

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicants: R. Bhattacharya et al.
Case: 1-4-2-2-1
Serial No.: 10/620,045
Filing Date: July 15, 2003
Group: 2123
Examiner: Mary C. Jacob

Title: Method and Apparatus for Automatic Generation
of Multiple Integrated Circuit Simulation Configuration

RESPONSE TO OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The remarks below are submitted in response to the final Office Action dated November 17, 2006 in the above-identified application.